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APPLICANTS Leo Matthew et al. GROUP ART UNIT: 2815
APPLN. NO.: 10/074,732 EXAMINER: Paul E. Brock II
FILED: February 13, 2002
TITLE: METHOD OF FORMING A VERTICAL DOUBLE GATE
SEMICONDUCTOR DEVICE AND STRUCTURE THEREOF

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AMENDMENT IN FURTHERANCE OF RCE UNDER 37 CFR 1.114

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to a final rejection in an Office action mailed April 20, 2004, Applicants are filing a Request for Continued Examination under 37 CFR 1.114 of U.S. Serial No. 10/074,732 and request entrance of the following amendment in view of the remarks presented thereafter.

FEEs

An additional fee of \$86 is believed to be owed in connection with this response since the total number of independent claims exceeds three. Applicants continue to authorize the payment of any fees owed in connection with this application from Deposit Account No. 503079 (Freescall Semiconductor), or credit Deposit Account No. 503079 for any refunds.